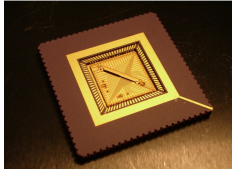


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## SCALABLE MICROCHIP ION TRAPS FOR QUANTUM COMPUTATION EBOOKS 2019



Scalable Microchip Ion Traps  
for Quantum Computation  
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The development of scalable microfabricated ion traps with multiple segments for the realization of quantum computing is a challenging task in quantum information science. The research on the design, development, fabrication, and operation of the first European micro-trap is shown in this thesis. This chip-based micro-trap is an outstanding candidate towards experiments for a future quantum processor with trapped single ions. In the experiments coherent quantum state manipulation is demonstrated, and sideband cooling to the motional ground state is realized. The heating rate is determined and the applicability for quantum computation is proven. Furthermore planar trap designs are investigated - a planar microparticle trap was built and operated. A linear microfabricated planar trap was operated, showing the proof of concept of a novel designed and fabricated Y-shaped planar trap.

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